Apparatus for the implementation of a system for the secure exchange of data according to the RSA method limited to digital signatures and message verification.

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Inventor: FERREIRA RONALD (FR); HOPPE JOSEPH (FR)
Applicant: TRT TELECOM RADIO ELECTR (FR); PHILIPS ELECTRONICS NV (NL)
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Cited documents:
XP000383974
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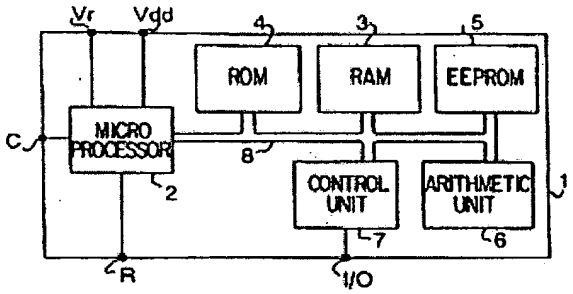
Abstract of **EP0675614**

The device uses a physically secure microcomputer (1) which includes a microprocessor (2), a volatile memory (3) and a permanent memory (4) holding the operating instructions. An EEPROM memory (5) contains the secret code for the card, and the public code required for information exchange.
The device uses RSA encryption coding and requires authentication of a digital signature before encoding or decoding of the data. Each message must have a required structure before it can be transmitted, and application of the signature to the message only occurs if the message structure has been validated.



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